

**AMENDMENTS TO THE SPECIFICATION**

Please replace the paragraph beginning on page 29, line 25, of the specification with the following rewritten paragraph.

The fuses latch circuit 108 includes a latch circuit 49, an inverter 43, p-channel transistors 24, 25, 26 and 27, and n-channel transistors 35, 36, 37, 38 and 39. The latch circuit 49 is composed of ~~AND~~ NAND circuits 45, 46. A sense amplifier 48 is composed of the p-channel transistors 26, 27 and the n-channel transistors 35, 36, 37 and 38. The sense amplifier 48 has a differential input and compares the potential of the contact B with the standard voltage  $V_{H-}$ . The p-channel transistors 24, 25 and the n-channel transistor 39, which are controlled by a sampling signal SE, control the operation of the fuse latch circuit 108.